UNITED STATES PATENT AND TRADEMARK OFFICE

BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 4630

, SERIAL NUMBER 10/632,866	FILING DATE 08/04/2003 RULE	CLASS 326	GROUP ART UNIT 2819	ATTORNEY DOCKET NO. 241190US2

APPLICANTS

Takeshi Yoshida, Kawasaki-shi, JAPAN;

** CONTINUING DATA ****************************
_Dc_   None

** FOREIGN APPLICATIONS *********************
JAPAN P2003-122256 04/25/2003
_Dc_

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 03/16/2004

Foreign Priority claimed ☑ yes ☐ no 35 USC 119 (a-d) conditions met ☑ yes ☐ no ☐ Met after Allowance Verified and Acknowledged _Examiner's Signature_ _Initials_	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 1

ADDRESS
22850
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.
1940 DUKE STREET
ALEXANDRIA , VA
22314

TITLE
Semiconductor integrated circuit having system bus divided in stages

FILING FEE RECEIVED 880	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	☐ All Fees
		☐ 1.16 Fees (Filing)
		☐ 1.17 Fees (Processing Ext. of time)
		☐ 1.18 Fees (Issue)
		☐ Other _____
		☐ Credit